

LAB EXAM SCHEDULE

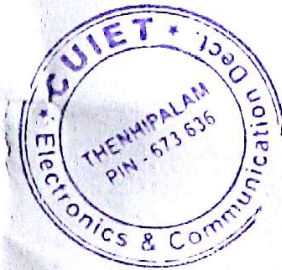
BRANCH : ELECTRONICS AND COMMUNICATION

SEMESTER&SCHEME: S3 ,2009

SUB:EE09 308(P)- ANALOG ELECTRONICS Lab

DATE 23/06/2017

SEMESTER	SUBJECT	DATE	TIME	REG.NO
S3	EE09 308(P)- ANALOG ELECTRONICS Lab	23-06-2017	10.00 to 01.00	IEALEEE051 IEAMEEE027 IEANEEE031 IEANEEE058



anj
HOD

Head of Department,
Electronics & Communication
Institute of Engg. & Technology
Calicut University